

--59. A network as in claim 58 wherein the plurality of LxL interconnect modules comprises  $\left(\frac{K}{L}\right)^2$  modules.--

--60. A network as in claim 58 which includes N1 input switches.--

--61. A network as in claim 60 which includes N2 output switches.--

--62. A network as in claim 58 wherein connectivity between the inputs, the modules and the outputs is symmetrical relative to a selected centerline.--

--63. A network as in claim 42 wherein each KxK module comprises:

a body portion which includes a plurality of LxL signal coupling networks with

K input ports coupled to the body portion;

K output ports coupled to the body portion; and

a plurality of signal paths, carried by the LxL signal coupling networks, the signal paths couple the input ports to the output ports.--

--64. A network as in claim 63 wherein the plurality of signal paths comprises  $K^2$  paths.--

--65. A network as in claim 63 wherein the signal paths comprise one of optical fibers or electrical conductors.--

--66. A network as in claim 42 wherein N1 inputs comprise  $\frac{N1}{K}$  groups of signal

carriers coupled to a corresponding number of KxK modules.--

#### R E M A R K S

Responding to the Examiner's Restriction requirement, Applicant hereby elects Group V, claims 41-44, with traverse, for initial prosecution. Claims 57-66, also in Group V have been added hereby. Entry of claims 57-66 prior to examination is hereby requested. A marked copy of the amended claim is attached.